

Digital Blocks Extends the DB9000 TFT LCD Controller IP Core Family with the availability of the DB9000AXI for the AMBA 3.0 Interconnect

Specifically targeted for large resolution TFT LCD panels and the ARM AMBA 3.0 AXI Bus, the DB9000AAX is an out-of-the-box synthesizable soft IP Core for display system designers.

GLEN ROCK, New Jersey, Nov 18, 2008 – Digital Blocks, a leading developer of silicon-proven semiconductor Intellectually Property (IP) soft cores for embedded processor and video system designers, today announces the DB9000AXI TFT LCD Controller IP Core. The DB9000AXI IP Core targets systems-on-chip (SoC) ASSP and ASIC designs containing embedded processors and the AMBA 3.0 AXI on-chip bus and system requirements for a TFT LCD panel.

The DB9000AXI IP Core specifically and cost-effectively targets TFT LCD panels with 1 Port of 18-bit digital (6-bits/color) or 24-bit digital (8-bits/color) interface or a 2 Port interface, with each port up to 24-bit digital (8-bits/color). The DB9000AXI, with its 64-bit AXI interface and programmable 2 Port TFT LCD Panel Interface, specifically targets high resolution displays.

While the DB9000AXI targets high resolution TFT LCD panels, the range of programming parameters permit support for a wide range of LCD panel resolutions. Representative examples are as follows:

Format	Resolution
Square	240x240
QVGA	320x240
	240x320
16:9 Aspect Ratio	480x272
VGA	640x480
SVGA	800x600
XGA	1024x768
SXGA	1280x1024
UXGA	1600x1200
WUXGA	1920x1200

DB9000 Family of TFT LCD Controllers

The DB9000 family of TFT LCD Controllers supports a variety of bus interfaces to frame buffer memory and processors. Please consult Digital Blocks web site for a complete listing.

Price and Availability

The DB9000AXI is available immediately in synthesizable Verilog, along with a simulation test bench with expected results, datasheet, and user manual. For further information, product evaluation, or pricing, please go to Digital Blocks at <http://www.digitalblocks.com>

About Digital Blocks

Digital Blocks designs silicon-proven IP cores for technology systems companies, reducing customer's development costs and significantly improving their time-to-volume goals. Digital Blocks is located at 587 Rock Rd, Glen Rock, NJ 07452 (USA). Phone: +1-201-251-1281; Fax: +1-201-632-4809; Media Contact: info@digitalblocks.com; Sales Inquiries: info@digitalblock.com; On the Web at www.digitalblocks.com

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