

Digital Blocks AMBA Multi-Channel DMA Controller IP Core Family Extends Leadership with enhancements to AXI4 Memory Map and Streaming Interfaces.

GLEN ROCK, New Jersey, January 2, 2023 – Digital Blocks, a leading developer of silicon-proven semiconductor Intellectually Property (IP) soft cores for system-on-chip (SoC) ASIC, ASSP, & FPGA developers, announces enhancements to DMA Controller Verilog IP Core offerings with capabilities to stream data to and from memory such as between Network Interfaces and System Memory.

Digital Blocks DMA Controller IP Core family members contain feature-rich, system integration-level features. Current DMA Controllers are as follows:

| DMA Controller Engines                                    |
|---|
| AXI4 Multi-Channel DMA Controller, 1-16 Channels,         |
| Scatter-Gather, high performance, many user feature-rich, |
| system integration-level options                          |
| AHB5 Multi-Channel DMA Controller – targets latest AHB    |
| Interconnect  |
|   |
| AXI4-Stream to Memory driven by DMA Controller            |
| Memory to AXI4-Stream driven by DMA Controller            |
|   |
| UDP/IP Hardware Stack with DMA Controller                 |
| DMA Controller with Interfaces to PCIe                    |

## **Price and Availability**

The Digital Blocks DMA Controller IP Core family is available in synthesizable Verilog, along with a comprehensive simulation test suite, datasheet, and user manual. For further information, product evaluation, or pricing, please go to Digital Blocks at https://www.digitalblocks.com/dma.html

## **About Digital Blocks**

Digital Blocks is a leading developer of silicon-proven semiconductor Intellectually Property (IP) soft cores for system-on-chip (SoC) ASIC, ASSP, & FPGA developers requiring best-in-class IP for AMBA Peripherals (DMA/I3C/I2C/SPI/eSPI Controllers), TFT LCD/OLED Display Controllers & Processors, 2D Graphics Hardware Accelerator Engines, Video Signal & Image Processing, and Low-Latency UDP/RTP Hardware Protocol Stacks.

Digital Blocks designs silicon-proven IP cores for technology systems companies, reducing customer's development costs and significantly improving their time-to-volume goals. Digital Blocks is located at 587 Rock Rd, Glen Rock, NJ 07452 (USA). Phone: +1-201-251-1281; Fax: +1-702-552-1905; Media Contact: <a href="mailto:info@digitalblocks.com">info@digitalblocks.com</a>; Sales Inquiries: <a href="mailto:info@digitalblock.com">info@digitalblocks.com</a>; On the Web at <a href="https://www.digitalblocks.com">www.digitalblocks.com</a>;