

Digital Blocks Delivers the DB1800 NTSC / PAL / SECAM Video Sync Separator IP Core

GLEN ROCK, New Jersey, August 16, 2006 – Digital Blocks, a leading developer of silicon-proven semiconductor Intellectually Property (IP) soft cores for embedded processor and video system designers, today announces the DB1800 NTSC/PAL/SECAM Video Sync Separator IP Core. The DB1800 is an all digital design for standard definition NTSC/PAL/SECAM VLSI video decoder applications.

The DB1800 accepts a NTSC/PAL/SECAM composite video signal, auto-detects the standard, and outputs the horizontal sync, vertical sync, chroma burst blanking, and field 1 or 2 (odd or even) detection. In addition, the DB1800 provides a display enable signal, which indicates which video lines and which segment of a video line contain active video.

Price and Availability

The DB1800 is available immediately in synthesizable Verilog, along with synthesis scripts, a simulation test suite, datasheet, and user manual. For further information, product evaluation, or pricing, please go to Digital Blocks at <http://www.digitalblocks.com>

About Digital Blocks

Digital Blocks designs silicon-proven IP cores for technology systems companies, reducing customer's development costs and significantly improving their time-to-volume goals.

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