

Digital Blocks Announces the Re-launch of its DB8255A Programmable Peripheral Interface IP Core

Originally Marketed through a Business Partner, Digital Blocks offers the DB8255A Direct.

GLEN ROCK, New Jersey, June 30, 2006 – Digital Blocks, a leading developer of silicon-proven semiconductor Intellectually Property (IP) soft cores for embedded processor system designers, today announces the re-launch of its DB8255A Programmable Peripheral Interface core. The DB8255A is a full-featured equivalent to the industry standard Intel 8255A / 82C55A and Intersil 82C55A used today with 8/16/32 microprocessor designs.

The DB8255A offers 24 general purpose programmable I/O lines for Intel, PowerPC, MIPS, ARM, Freescale, and emerging microprocessor architectures. The DB8255A is a silicon-proven IP core for new microprocessor designs requiring programmable I/O or for higher integration systems incorporating an 8255A device.

The DB8255A is available immediately in synthesizable VHDL, along with synthesis scripts, a comprehensive simulation test suite, datasheet, and user manual.

About Digital Blocks

Digital Blocks designs silicon-proven IP cores for technology systems companies, reducing customer's development costs and significantly improving their time-to-volume goals.

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