Digital Blocks Announces the Re-launch of its DB8259A and DB8259S Programmable Interrupt Controller IP Cores

Originally Marketed through a Business Partner, Digital Blocks offers the DB8259A and DB8259S Direct.

GLEN ROCK, New Jersey, February 6, 2006 – Digital Blocks, a leading developer of silicon-proven semiconductor Intellectually Property (IP) soft cores for embedded processor system designers, today announces the re-launch of its DB8259A and DB8259S Programmable Interrupt Controller cores. The DB8259A and DB8259S are full-featured equivalents to the industry standard Intel 8259a and Harris 82C59A interrupt controllers, used today with 8/16/32 microprocessor designs.

The DB8259A maintains the original static design of the Intel 8259a and Harris 82C59A devices. The DB8259S adds a clock for an all synchronous design.

"The DB8259A is a feature-rich programmable interrupt controller for Intel, PowerPC, MIPS, and emerging microprocessor architectures while the DB8259S is an all synchronous implementation," said Steven Stein, President of Digital Blocks. "With our eight years experience developing and supporting the DB8259 family, Digital Blocks has enabled 20+ customers bring their embedded processor systems with Programmable Interrupt Controller requirements to market faster and at less cost."

The DB8259A and DB8259S are available immediately in synthesizable VHDL and Verilog, along with synthesis scripts, a comprehensive simulation test suite, datasheet, and user manual.

About Digital Blocks

Digital Blocks designs silicon-proven IP cores for technology systems companies, reducing customer's development costs and significantly improving their time-to-volume goals. Digital Blocks is located at 587 Rock Rd, Glen Rock, NJ 07452 (USA).
Phone: 1-201-251-1281; Fax: 1-208-379-1012; Media Contact: info@digitalblocks.com;
Sales Inquiries: info@digitalblock.com; On the Web at www.digitalblocks.com