# Digital Blocks Announces the AMBA Multi-Channel DMA Controller IP Core

*The Digital Blocks DB-DMAC-MC-AMBA IP Supports 1 – 32 independent data block / packet / stream transfers in parallel, providing low and high performance data transfer rates among AXI, AHB, APB interconnect memories and peripherals as well as high and low performance Peripherals with unique or custom interfaces.*

**GLEN ROCK, New Jersey, October 4, 2013** – Digital Blocks, a leading developer of silicon-proven semiconductor Intellectually Property (IP) soft cores for system-on-chip (SoC) ASIC, ASSP, & FPGA developers with Embedded Processor & Peripherals requirements, today announces the General Availability of its AMBA Bus Multi-Channel DMA Controller IP successfully used within a number of Digital Blocks IP Cores.

Each 1 - 32 internal Direct Memory Access (DMA) Controller Engine services each interface at its maximum throughput, whether it's an AXI interconnect with high data burst and width capability, or a Peripheral with slower speed, narrower data width requirements. The DMA Controller IP Core can serve as a general-purpose Programmable DMA Controller supporting many system memories and peripherals, or be sized to the user required number of DMA Engines, AMBA interconnect interfaces, and user application interfaces. For further technical information, please go to the data sheet on Digital Blocks site at http://www.digitalblocks.com/files/DB-DMAC-MC-AMBA-DS-V1_1.pdf

**Price and Availability**
The DB-DMAC-MC-AMBA DMA Controller IP Core is available in synthesizable Verilog, along with a comprehensive simulation test suite, datasheet, and user manual. For further information, product evaluation, or pricing, please go to Digital Blocks at www.digitalblocks.com

**About Digital Blocks**
Digital Blocks is a leading developer of silicon-proven semiconductor Intellectually Property (IP) soft cores for system-on-chip (SoC) ASIC, ASSP, & FPGA developers with Embedded Processor & Peripherals, Display Controller, Display Link Layer, 2D Graphics, Image Compression, Audio / Video processing, and High-Speed Audio/Video Networking / High-Frequency Trading Networking requirements.

Digital Blocks designs silicon-proven IP cores for technology systems companies, reducing customer's development costs and significantly improving their time-to-volume goals. Digital Blocks is located at 587 Rock Rd, Glen Rock, NJ 07452 (USA). Phone: +1-201-251-1281; Fax: +1- 702-552-1905; Media Contact: info@digitalblocks.com; Sales Inquiries: info@digitalblock.com; On the Web at www.digitalblocks.com

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