



## **Digital Blocks Announces 2nd Gen Audio/Video & Data Hardware Protocol Stacks Supporting MPEG2 Transport Stream (TS), RTP, and UDP/IP Protocols**

**GLEN ROCK, New Jersey, March 24, 2015** – Digital Blocks, a leading developer of low-latency, high performance networking IP Cores for Video & Data Networking Applications, today announces 2nd Generation Hardware Protocol Stacks supporting a mix of MPEG2 Transport Stream (TS), RTP, and UDP/IP Protocols for network adapter cards with 10/100/1000 Mbps or 10/40/100 Gbps network links.

The following tables list Digital Blocks Hardware Protocol Stacks IP Cores:

<b>IP Core</b>	<b>Hardware Protocol Stack Support</b>
DB-UDP-IP	UDP/IP
DB-RTP-UDP-IP	RTP/UDP/IP
DB-MPEG-TS-UDP-IP	MPEG-TS/UDP/IP
DB-MPEG-TS-RTP-UDP-IP	MPEG-TS/RTP/UDP/IP

Note that all Digital Blocks Hardware Protocol Stacks support Transmit (Encapsulation) & Receive (Decapsulation) as well as Transmit-only or Receive-only configurations.

### **Price and Availability**

Digital Blocks Protocol Hardware Stack IP Cores are available immediately in synthesizable Verilog, along with a simulation test bench with expected results, datasheet, and user manual. For further information, product evaluation, or pricing, please go to Digital Blocks at <http://www.digitalblocks.com>

### **About Digital Blocks**

Digital Blocks designs silicon-proven IP cores for technology systems companies, reducing customer's development costs and significantly improving their time-to-volume goals. Digital Blocks is located at 587 Rock Rd, Glen Rock, NJ 07452 (USA). Phone: +1-201-251-1281; eFax: +1-702-552-1905; Media Contact: [info@digitalblocks.com](mailto:info@digitalblocks.com); Sales Inquiries: [info@digitalblock.com](mailto:info@digitalblock.com); On the Web at [www.digitalblocks.com](http://www.digitalblocks.com); Twitter at [twitter.com/DigitalBlocksIP](https://twitter.com/DigitalBlocksIP)

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